

## BAR CODE LABEL



## U.S. PATENT APPLICATION

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## \*\*CONTINUING DATA\*\*\*\*\*

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## \*\*FOREIGN/PCT APPLICATIONS\*\*\*\*\*

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## TITLE

FLEXIBLE, HIGH-PERFORMANCE STATIC RAM ARCHITECTURE FOR  
 FIELD-PROGRAMMABLE GATE ARRAYS

This is to certify that annexed hereto is a true copy from the records of the United States Patent and Trademark Office of the application which is identified above.

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Date

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